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10CS46

Fourth Semester B.E. Degree Examination, Dec.2016/Jan.2017
Computer Organization

Time: 3 hrs.

Max. Marks:100

**Note: Answer any FIVE full questions, selecting
atleast TWO questions from each part.**

PART – A

- 1 a. What is pipelining? How does it improve the performance of the computer? (08 Marks)
 b. Compare CISC versus RISC processors. (04 Marks)
 c. Explain clearly SPEC rating and its significance. (08 Marks)
- 2 a. What is the need of an addressing mode? Explain three addressing modes with examples. (08 Marks)
 b. Convert the following numbers to signed, 2's complement binary number and add them.
 (i) 7 and -5 (ii) -10 and -13 (04 Marks)
 c. What is stack frames? Explain. (04 Marks)
 d. Explain the shift and rotate operations with example. (04 Marks)
- 3 a. In modern computers, why interrupts are required? Support your claim with a suitable example. (04 Marks)
 b. Showing the possible register configuration in DMA interface, explain direct memory access. (08 Marks)
 c. With a neat sketch, explain the individual input and output interface circuits. Also elicit their salient features. (08 Marks)
- 4 a. Write a note on PCI configuration and explain with neat figure the single processor system configurations. (08 Marks)
 b. Explain the different phases in the operations of SCSI bus speed in detail. (06 Marks)
 c. Explain the following : (06 Marks)
 (i) USB addressing (ii) USB protocols.

PART – B

- 5 a. Describe SDRAM and DDR SDRAM operations for data transfer between main memory and cache memory systems. (08 Marks)
 b. Explain any one cache mapping function. (06 Marks)
 c. Consider a two level cache with access times of 5 ns and 80 ns respectively. If the hit rates are 95% and 75% respectively in the two caches and the memory access time is 250 ns, what is the average access time? (04 Marks)
 d. Calculate the effective address time if average page fault service time of 20 milliseconds and a memory access time of 80 nano seconds. (Assume the probability of a page fault as 10%). (02 Marks)
- 6 a. Explain how a 16-bit carry look ahead adder can be built from a 4-bit adder. (08 Marks)
 b. Using the non storing division algorithm, perform the division of numbers 23 by 5 (23 ÷ 5). (08 Marks)
 c. Explain the IEEE standards for floating point number. (04 Marks)



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- 7 a. Explain the process of fetching a word from memory using timing diagram of memory read operations, with an example. (08 Marks)
- b. Bring out any four difference between hardwired and microprogrammed control. (04 Marks)
- c. With a neat diagram, explain the microinstruction sequencing organization. (08 Marks)
- 8 a. Write a short note on power wall. (06 Marks)
- b. State and explain the Amdahl's law and compute the speed up gained for the following. Suppose that the new CPU is 10 times faster in computing floating point calculations and old CPU is busy with floating point calculations 40% of the time. Calculate speed up gained by the new CPU. (08 Marks)
- c. With a neat block diagram bring out the characteristics of shared memory multiprocessors (SMPs). (06 Marks)

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